

### REMARKS

Applicants respectfully request reconsideration of the present U.S. Patent application. Claims 13, 15, 16 and 20 have been amended. Claims 1-12, 17, 18, 21 and 25 have been cancelled without prejudice. Claims 26-28 have been added. Therefore, claims 13-16, 19, 20, 22-24 and 26-28 are pending.

Applicant has canceled claims 1-12, 17, 18, 21 and 25 without prejudice in order to expedite allowance of the present application. Claims 1-12, 17, 18, 21 and 25 have not been canceled to overcome the cited references and the cancellation of claims 1-12, 17, 18, 21 and 25 is not to be interpreted as having been made to overcome the cited references.

#### Allowed Claims

Claims 14 and 19 have been allowed. Claims 13, 15 and 16 have been amended to depend from claim 14. Because dependent claims necessarily include the limitations of the claims from which they depend, Applicants respectfully submit that claims 13, 15 and 16 are in condition for allowance in addition to claims 14 and 19.

#### Claim Rejections - 35 U.S.C. § 102

Claims 1, 2, 4, 6, 7, 9, 11-13, 17, and 18 were rejected under 35 U.S.C. § 102(b) as being anticipated by U.S. Patent No. 5,652,823 issued to Eto et al. (*Eto*). Claims 1, 2, 4, 6, 7, 9, 11, 12, 17, and 18 have been cancelled without prejudice. Therefore, Applicants submit the rejection of these claims is moot. Claim 13 has been amended to depend from claim 14, which has been indicated as allowable. Therefore, Applicants submit that claim 13 is likewise allowable.

### Claim Rejections - 35 U.S.C. § 103

Claims 3, 5, 8, 10, 15, and 16 were rejected under 35 U.S.C. § 103(a) as being unpatentable over *Eto* in view of U.S. Patent No. 5,892,518 issued to Mizobata et al. (*Mizobata*). Claims 3, 5, 8 and 10 have been cancelled without prejudice. Therefore, Applicants submit that the rejection of these claims is moot. Claims 15 and 16 have been amended to depend from claim 14, which has been indicated as allowable. Therefore, Applicants submit that claims 15 and 16 are likewise allowable.

Claims 20-23 were rejected under 35 U.S.C. § 103(a) as being unpatentable over *Eto* in view of U.S. Patent No. 6,208,350 issued to Herrera (*Herrera*). Claim 20 has been amended to recite limitations similar to the limitations of claim 14. Claim 21 has been canceled without prejudice. Claims 22 and 23 depend from claim 20. Therefore, Applicants submit that claims 20, 22 and 23 are allowable over the references relied upon.

Claims 24 and 25 were rejected under 35 U.S.C. § 103(a) as being unpatentable over *Eto* and *Herrera* in view of U.S. Patent No. 5,446,495 issued to Tourtier et al (*Tourtier*). Claim 24 depends from claim 20. Claim 25 has been canceled without prejudice. Therefore, Applicants submit that claim 24 is allowable over the references relied upon.

### New Claims

New claims 26-28 have been added. Claim 26 is a method claim and recites limitations similar to claim 14, which has been allowed. Claims 27-28 depend from claim 26. Therefore, Applicants submit that claims 26-28 are also allowable over the references relied upon.

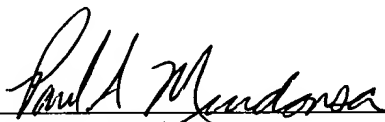
Conclusion

For at least the foregoing reasons, Applicants submit that the rejections have been overcome. Therefore, claims 13-16, 19, 20, 22-24 and 26-28 are in condition for allowance and such action is earnestly solicited. The Examiner is respectfully requested to contact the undersigned by telephone if such contact would further the examination of the present application.

Please charge any shortages and credit any overcharges to our Deposit Account number 02-2666.

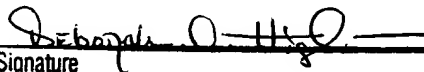
Respectfully submitted,  
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Date: JAN 16, 2002

  
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MARKED VERSION OF THE AMENDED CLAIMS

13. (Amended) The circuit of claim 14 [11] wherein the first order corresponds to an output sequence of an inverse discrete cosine transform operation.

15. (Amended) The circuit of claim 14 [11] wherein the processing circuitry comprises a setup engine that determines a bounding box for pixels manipulated by the instruction, wherein the bounding box contains all edges of a macroblock.

16. (Amended) The circuit of claim 14 [11] wherein the processing circuitry comprises a windower having a first mode wherein pixels inside a triangle within a bounding box are processed, and a second mode wherein all pixels within the bounding box are processed.

20. (Amended) An apparatus comprising:

a command stream controller coupled to receive an instruction to manipulate motion compensation video data;

a memory coupled to the command stream controller, the memory to store pixel data related to a macroblock in a first order, the first order is based on output from an Inverse Discrete Cosine Transform (IDCT) operation;

a read address generator coupled to the memory, the read address generator to cause the memory to output the pixel data related to a macroblock in a second order, the read address generator to cause the memory to output pixel data in sub-block-by-sub-block row major order;  
and

a processing unit coupled to the read address generator and to the command stream controller, the processing unit to perform motion compensation operations and texture mapping operations utilizing common circuitry.